

Figure 1

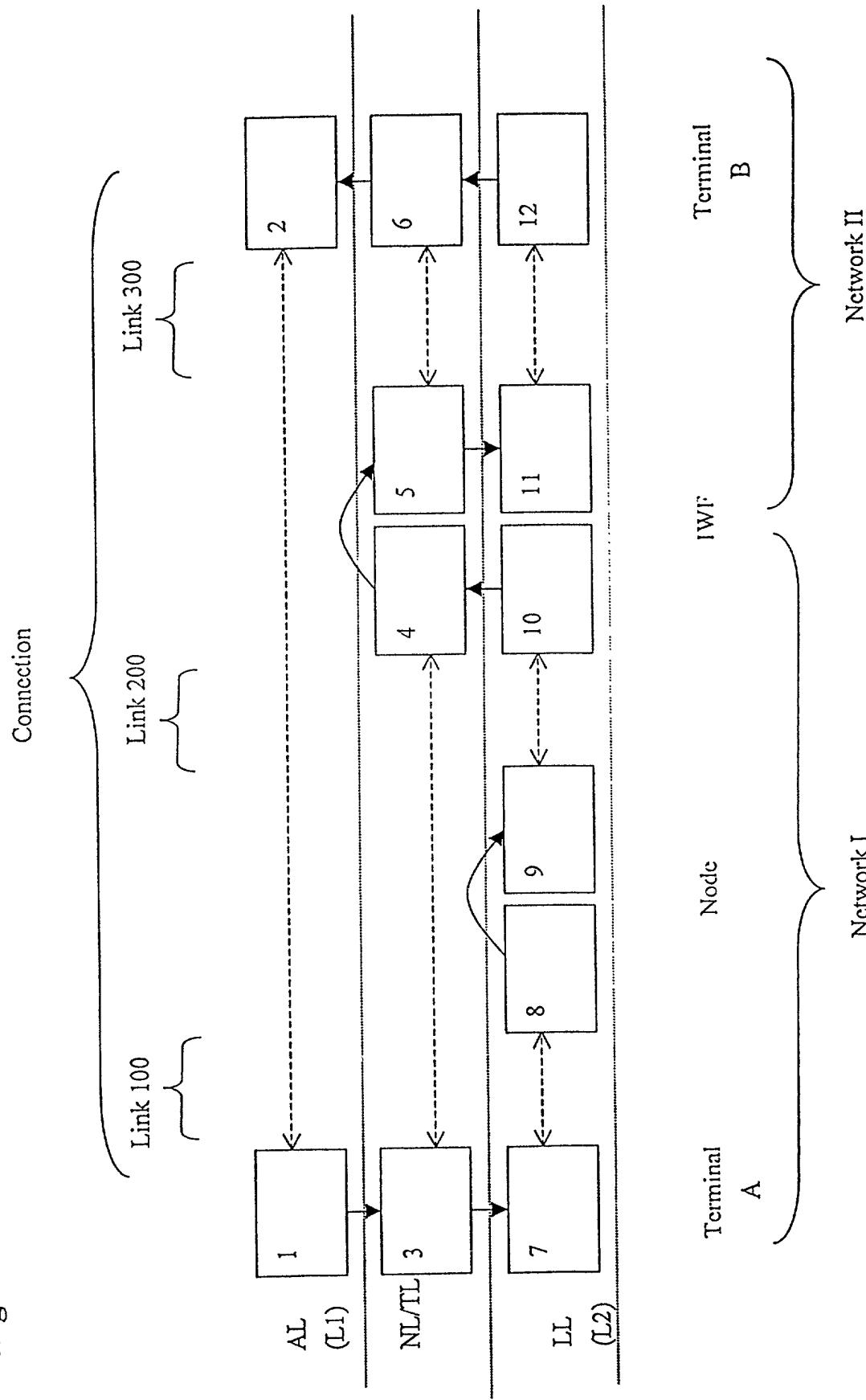


Figure 2a

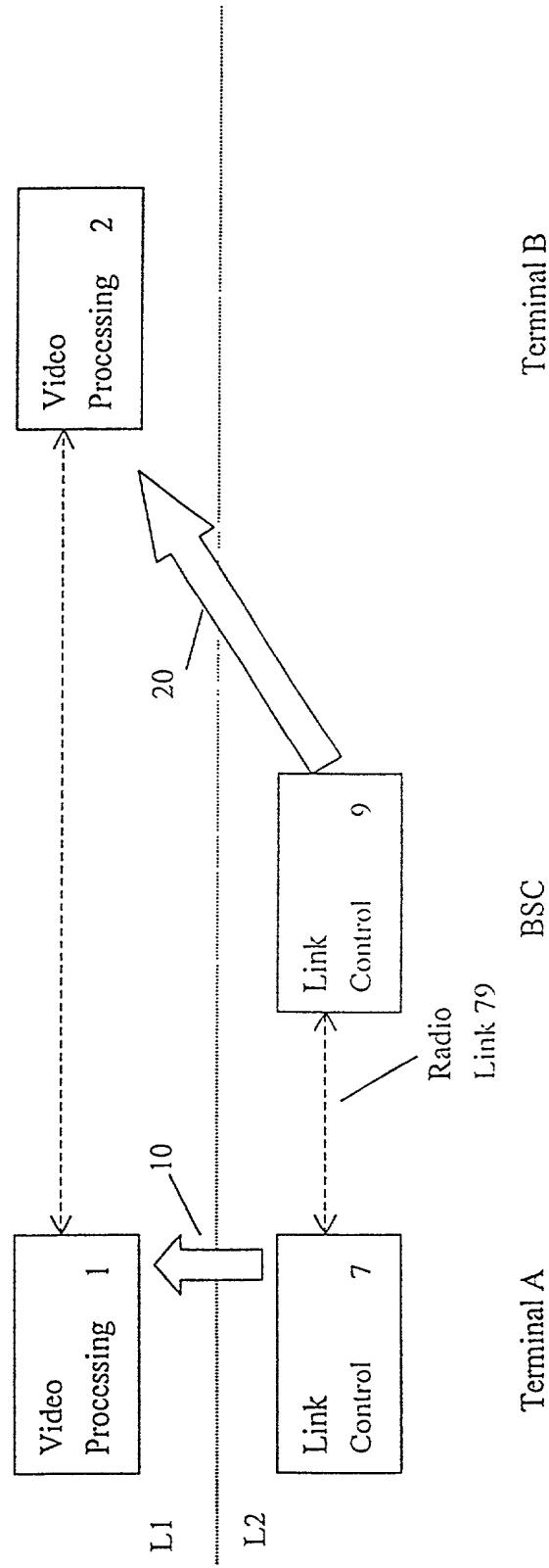


Figure 2b

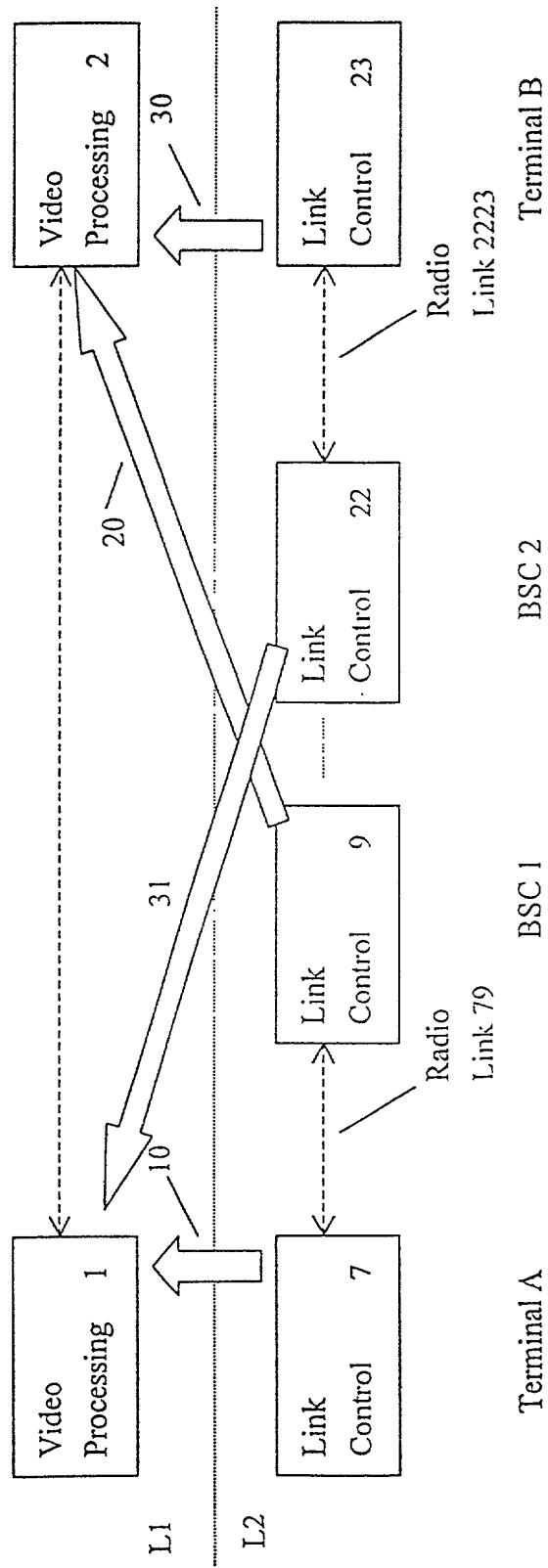


Figure 3a

Figure 3a illustrates the architecture of a video processing system, showing the flow of data and control between various components. The components are arranged in a vertical stack, connected by dashed lines representing data paths and solid lines representing control paths.

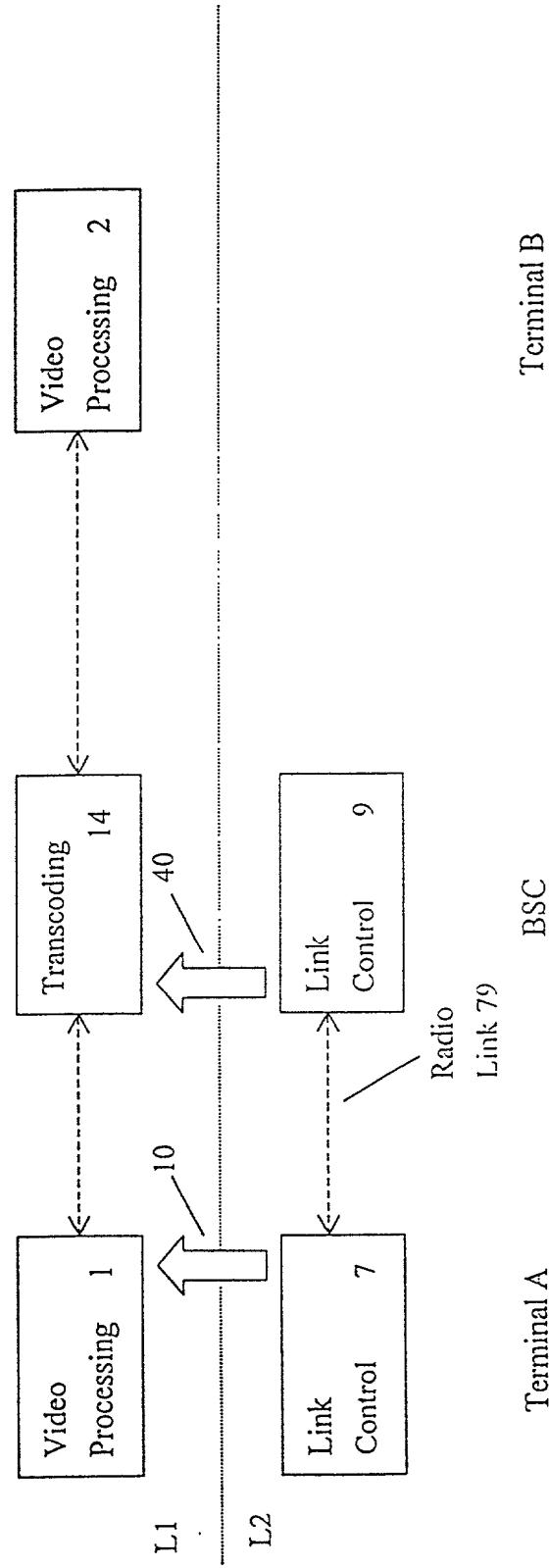
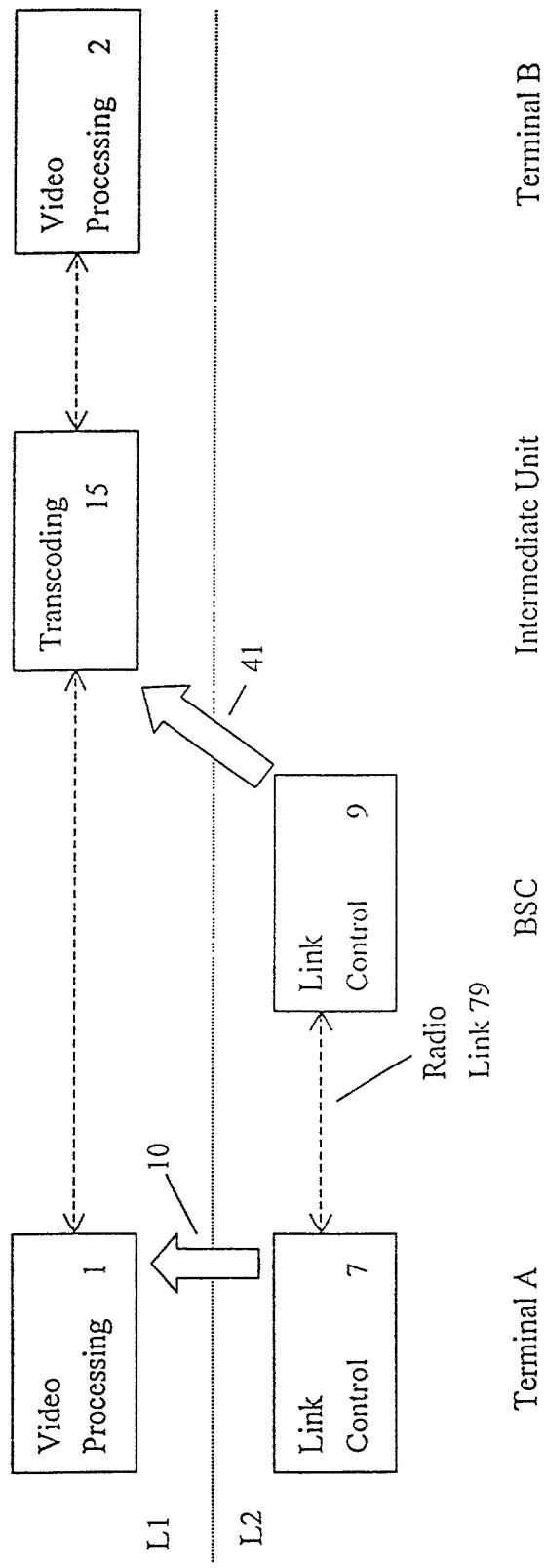


Figure 3b



50 100 150 200 250 300 350 400 450 500 550 600 650 700 750 800 850 900 950

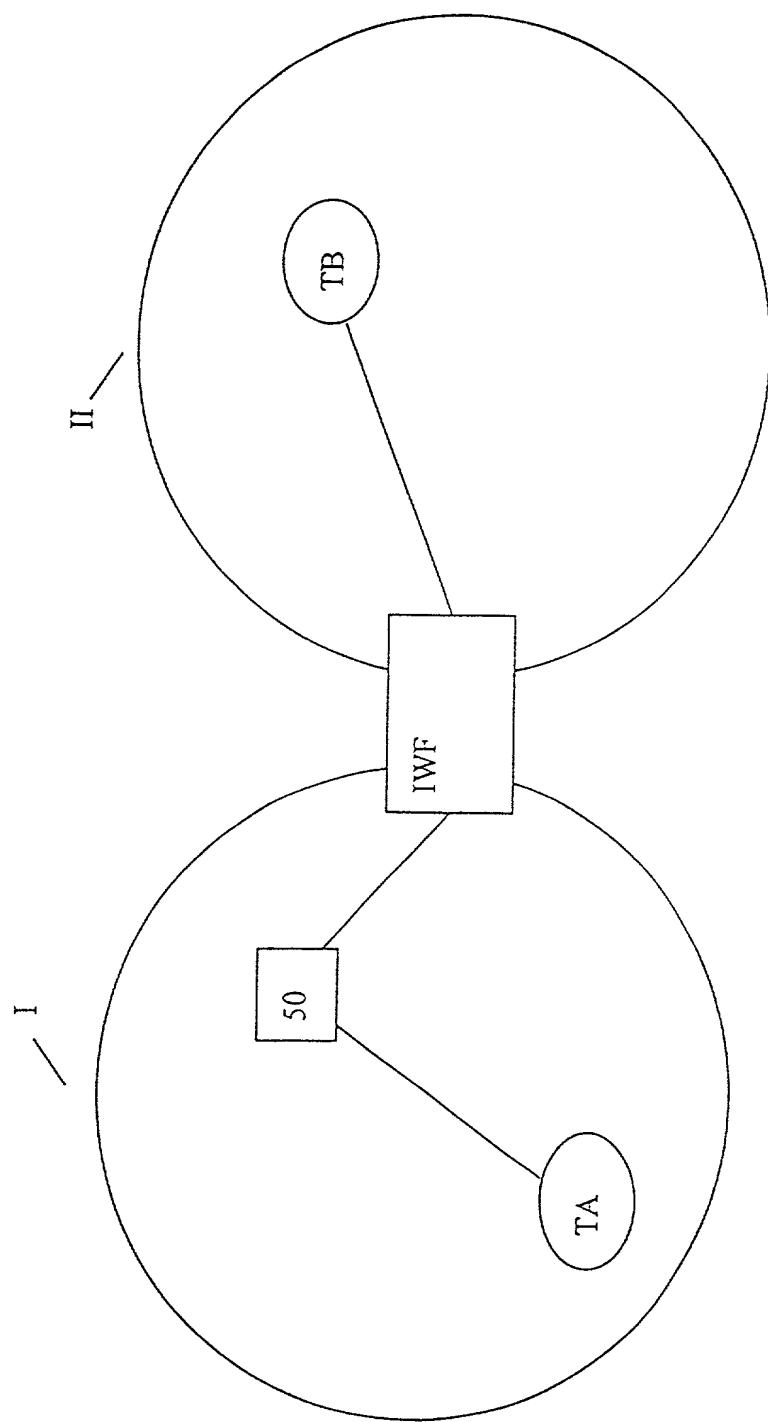
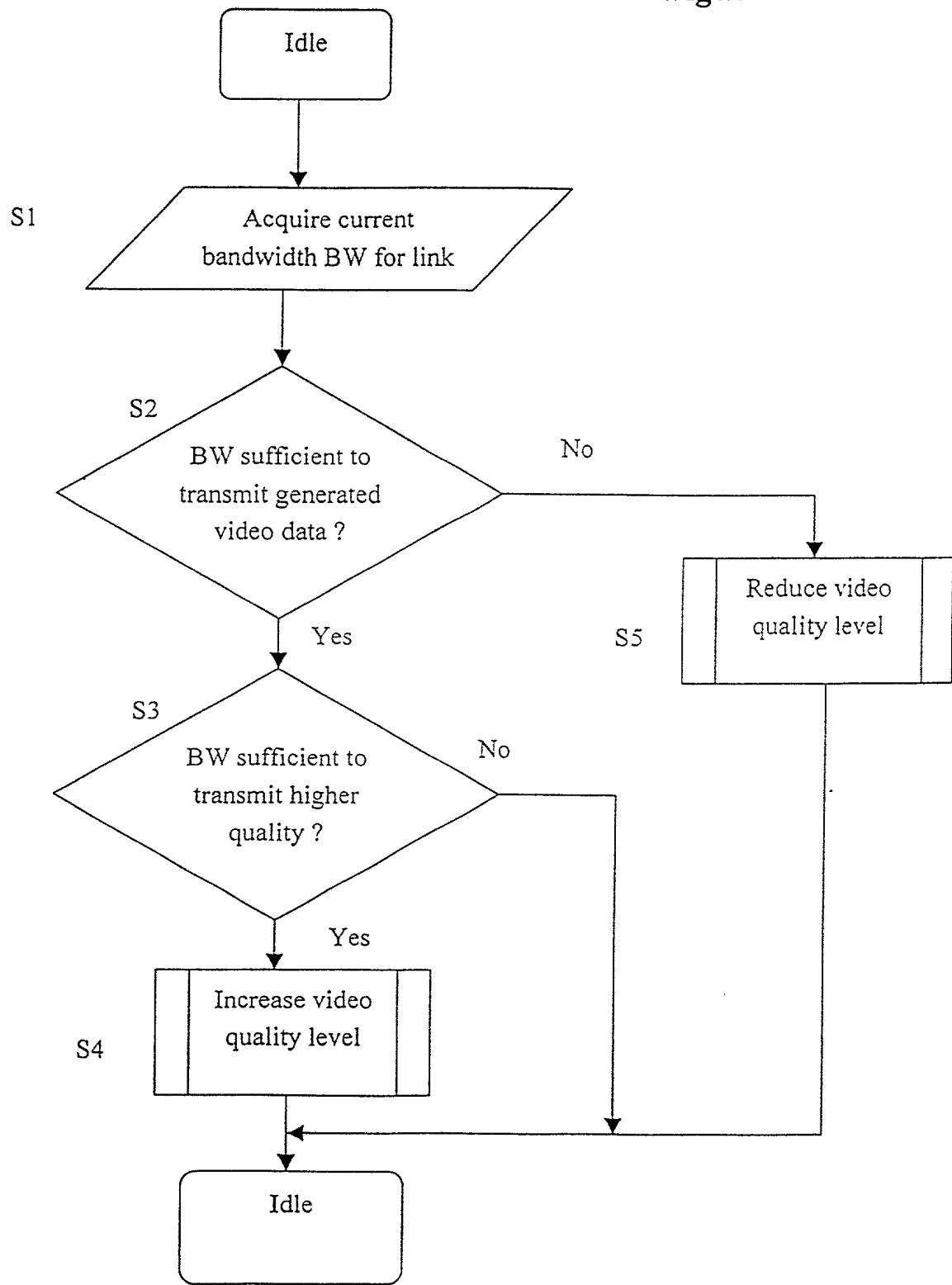


Figure 4

Figure 5



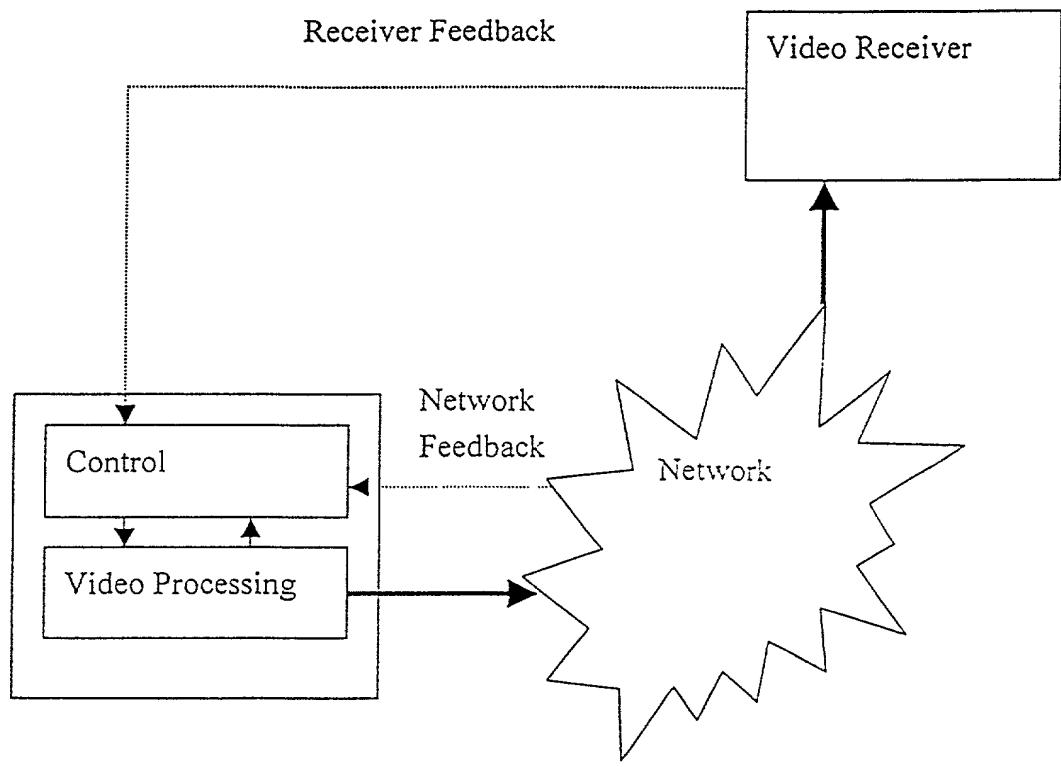


Figure 6

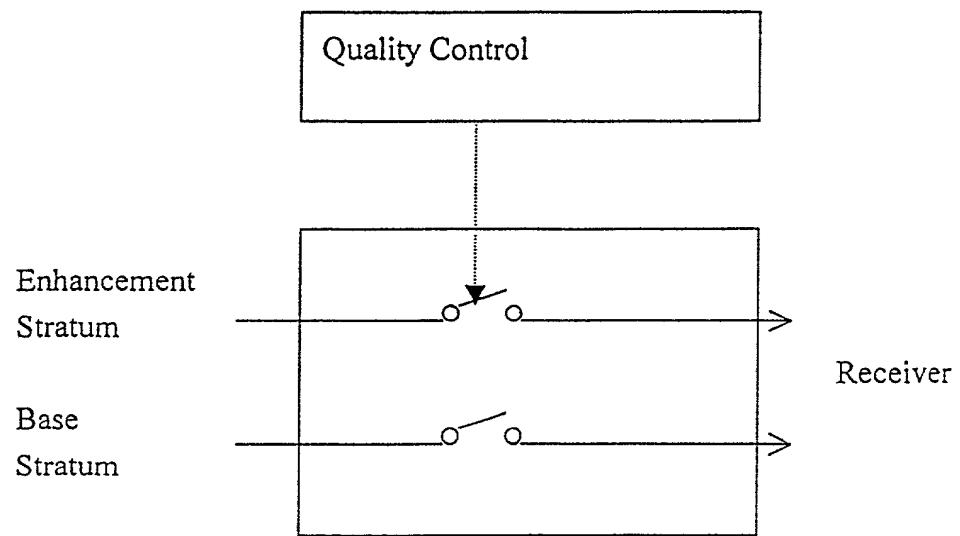


Figure 7

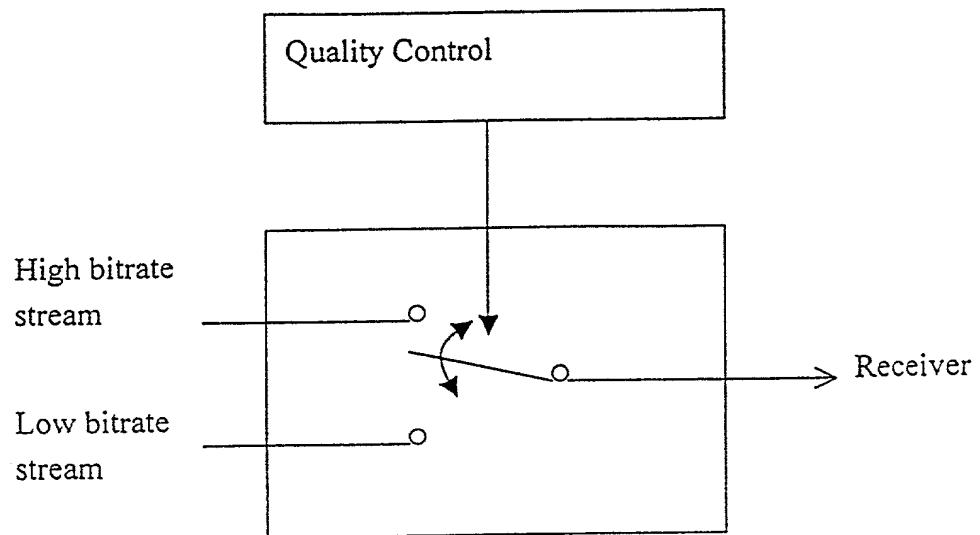


Figure 8